

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	("20050157046").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/07/10 09:46
L3	0	"347"/\$.ccls. and (module with (circuit near board) with recess \$4) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 10:56
L4	0	"347"/\$.ccls. and (module with (circuit near5 board) with recess \$4) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 10:56
L5	0	"347"/\$.ccls. and (module with (circuit near5 board) with recess) and Silverbrook. IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 10:56
L6	9	"347"/\$.ccls. and (module with (circuit pcb) with recess\$3) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 10:57
L7	7	("5148194" "5245361" "5565900" "5903295" "5969730" "6290334" "6655786").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/07/10 10:59
L8	26	("6655786").URPN.	USPAT	OR	OFF	2009/07/10 11:15
L9	22	"347"/\$.ccls. and (controller with least with control with circuit).CLM. and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:20

L10	0	"347"/\$.ccls. and (controller with least with control with circuit with recess\$3). CLM. and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:29
L11	2	"347"/\$.ccls. and (controller same least same control same circuit same recess\$3). CLM. and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:30
L12	840	"347"/49.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:31
L13	50749	"347"/49.ccls. (pcb (circuit near board)) with (recess terminal contact) with (electric\$3 control \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:33
L14	118	"347"/49.ccls. and (pcb (circuit near board)) with (recess terminal contact) with (electric\$3 control \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:33
L15	197	"347"/\$.ccls. and ((tile module circuit pec pcb) with recess\$3 with (adjacent abutt\$3) with (connect\$3 electric\$3)) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 11:58
L16	205	"347"/\$.ccls. and ((tile module circuit pec pcb) with recess\$3 with (adjacent abutt\$3) with (connect\$3 electric\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:00
L17	8	16 not 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:01

L18	249	("347"/\$.ccls. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with recess\$3 with (adjacent abutt\$3) with (connect\$3 electric \$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:05
L19	11	("347"/\$.ccls. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with recess\$3 with (adjacent abutt\$3) with (connect\$3 electric \$3)) not Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:05
L20	1693	("347"/\$.ccls. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with (adjacent abutt\$3) with (connect\$3 electric\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:06
L21	2503	("347"/\$.ccls. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with (recess\$3 latch\$3 snap\$4 edge) with (adjacent abutt\$3 connect\$3 together) with (connect\$3 electric\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:08
L22	1777	("347"/\$.ccls. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with (recess\$3 latch\$3 snap\$4 edge) with (adjacent abutt\$3 connect\$3 together) with (connect\$3 electric\$3)) not Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:08
L23	1346	("347"/49.ccls. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with (recess\$3 latch\$3 snap\$4 edge) with (adjacent abutt\$3 connect\$3 together) with (connect\$3 electric\$3)) not Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:09

L24	726	("347"/\$.cds. or inkjet or ink-jet or (ink adj jet)) and ((tile module circuit pec pcb) with (recess\$3 latch\$3 snap\$4 edge) with (adjacent abutt\$3 connect\$3 together) with (connect\$3 electric\$3)) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:10
L25	238	("347"/\$.cds. or inkjet or ink-jet or (ink adj jet)) and (adjacent with pcb\$3 with electrical\$4 with connect\$3) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:18
L26	276	("347"/\$.cds. or inkjet or ink-jet or (ink adj jet)) and ((adjacent abutt\$3 attach\$4 interconnect\$3 adjoin\$3) with (circuit module pcb\$3) with electrical\$4 with connect\$3) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:39
L27	1202	("347"/\$.cds. or inkjet or ink-jet or (ink adj jet)) and ((adjacent abutt\$3 attach\$4 interconnect\$3 adjoin\$3) with (circuit module pcb\$3) with electrical\$4 with connect\$3) not Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:39
L28	38	26 not 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 12:39
L29	297	("347"/\$.cds. or inkjet or ink-jet or (ink adj jet)) and ((adjacent abutt\$3 attach\$4 interconnect\$3 adjoin\$3) with (circuit module pcb\$3) with electrical\$4 with connect\$3).QLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/07/10 13:00
L30	2	("7077495").PNL	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2009/07/10 13:37

7/ 10/ 2009 1:52:28 PM

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